

ENHANCED FUSE CONFIGURATIONS FOR LOW-VOLTAGE FLASH MEMORIES

Abstract

An enhanced fuse circuit is discussed that advances redundancy techniques in integrated circuits. The enhanced fuse circuit uses a single nonvolatile fuse and a latch that is coupled at a desired time. One embodiment of the invention discusses a fuse
5 circuit that includes a volatile latch and a nonvolatile fuse. The nonvolatile fuse adapts to operate with a voltage supply greater than about 1.65 volts. The voltage supply is boosted at a desired time to a predetermined level and for a predetermined duration so that the nonvolatile fuse transfers its data to the volatile latch.

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